

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	464	"sample and hold circuit"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L2	32104	(variable adj gain adj amplifier) vga	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L3	1	L2 with (first adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L4	7	L2 and (first adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L5	174	(first adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L6	190	(second adj sampl\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L7	90	L5 with L6	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L8	1	L7 with L2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55

L9	3	L2 and L6 and L7	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L10	15	L2 with (sampl \$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L11	13	L2 and (sampl \$3 adj switch) and (hold\$3 adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L12	8	L2 and (sample adj switch) and (hold adj switch)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L13	38	chou.in. and kuo-yu	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L14	4	L13 and L2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L15	12	"sample-hold- circuit"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L16	22247	(sample adj hold) near circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L17	819	L16 and (vga (variable adj gain adj amplifier))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55

L18	28	L17 and (sampl \$3 adj switch \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L19	2	L18 and (hold \$3 adj switch \$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L20	2	(L16 and (vga (variable adj gain adj amplifier))) and (sampl\$3 near switch) and (hold near switch\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L21	2	L18 and ((sampl \$3 adj pulse adj signal) or (hold \$3 adj pulse adj signal))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L22	888	(sampl\$3 adj pulse adj signal) or (hold \$3 adj pulse adj signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55
L23	7	L22 and L17	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2008/04/08 19:55

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